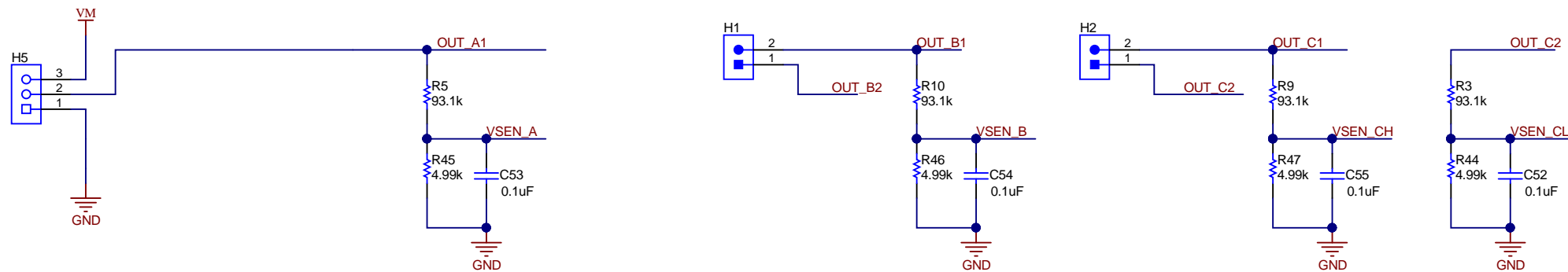


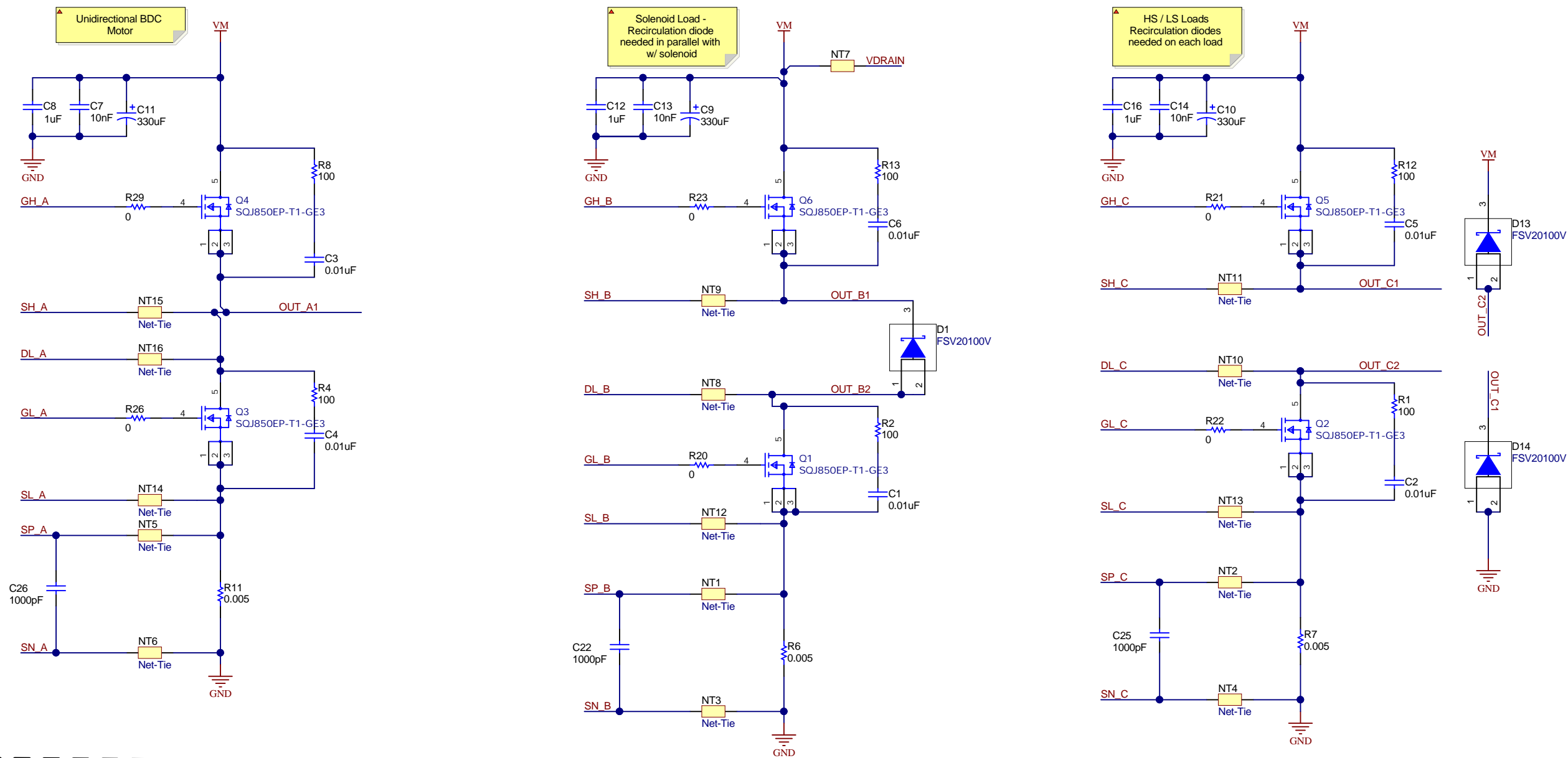
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TID #: N/A	Project Title: <a href="#">Change in menu Project Project Options Parameters</a>	
Number: XXX###	Rev: E1	Sheet Title: <a href="#">Complete Schematic</a>
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 1
Drawn By: Not shown in title block	File: <a href="#">TIDA_PWR_MANAGEMENT.SchDoc</a>	Size: B
Engineer: <a href="#">Enter name of project lead</a>	Contact: <a href="#">http://www.ti.com/support</a>	

OUTPUT CONNECTORS/V SENSE

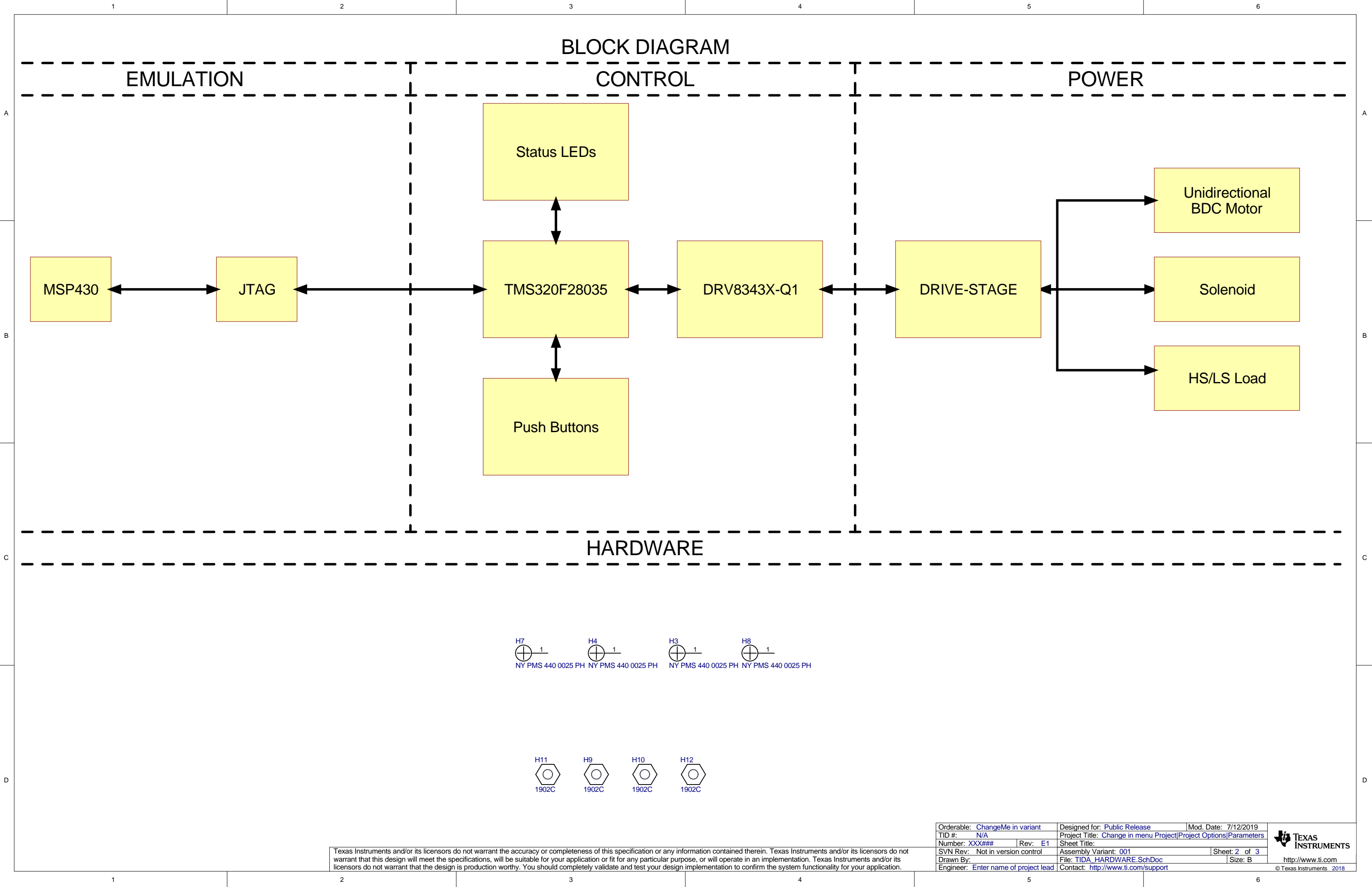


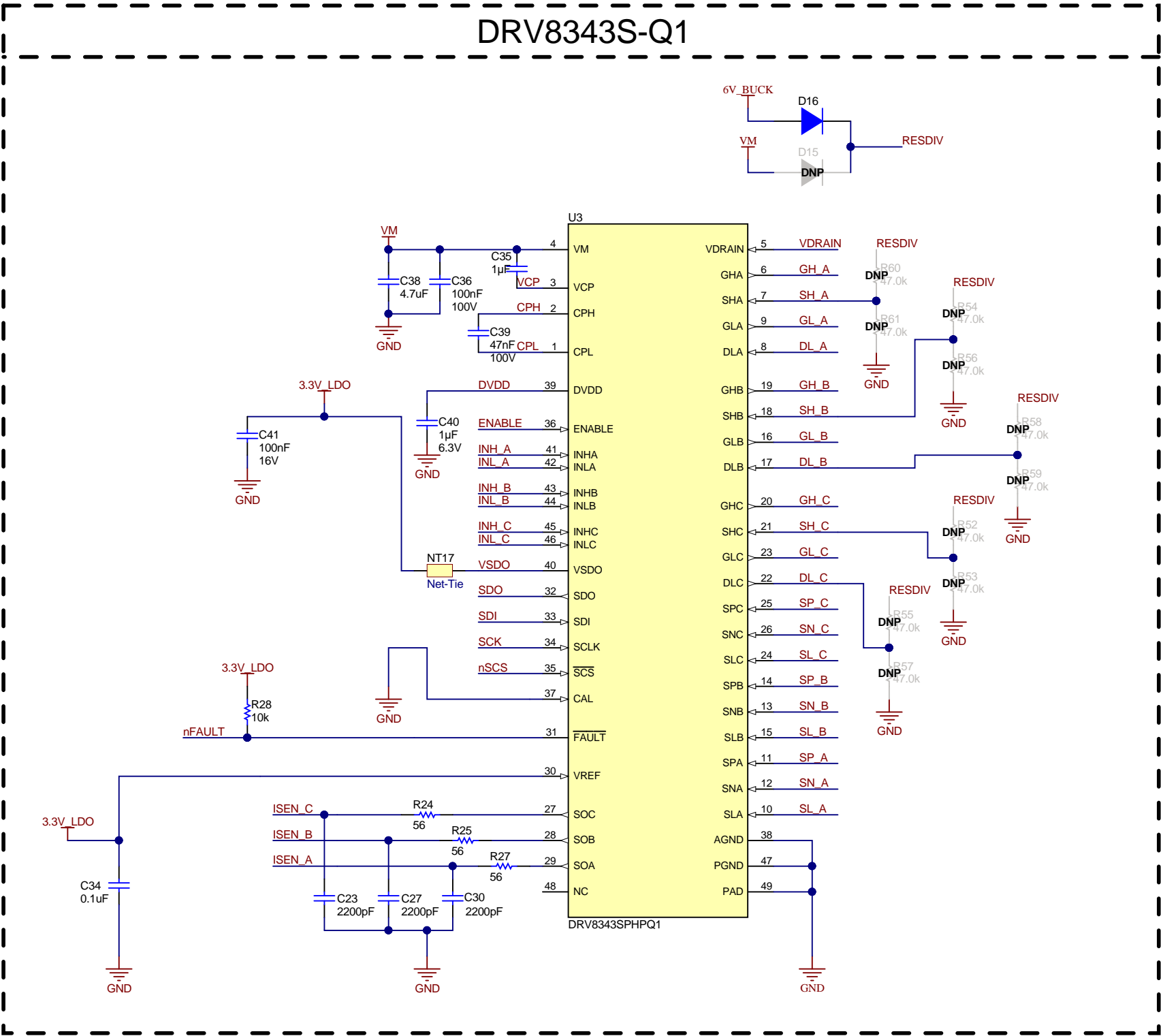
POWER STAGE



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TID #: N/A	Project Title: <a href="#">Change in menu Project/Project Options/Parameters</a>	
Number: XXX###	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: <a href="#">TIDA_POWERSTAGE_NO_TPS.SchDoc</a>	Size: B
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TID #: <a href="#">N/A</a>	Project Title: <a href="#">Change in menu Project</a>	<a href="#">Project Options</a> <a href="#">Parameters</a>
Number: <a href="#">XXX###</a>	Rev: <a href="#">E1</a>	Sheet Title:
SVN Rev: <a href="#">Not in version control</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">2</a> of <a href="#">3</a>
Drawn By:	File: <a href="#">TIDA_DRIVER.SchDoc</a>	Size: <a href="#">B</a>
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